

Product Features

- ✧ Dual data-rate of 1.25Gbps/1.063Gbps operation
- ✧ 1550nm DFB laser and PIN photo detector for 80km transmission
- ✧ 1490nm DFB laser and PIN photo detector for 80km transmission
- ✧ BIDI SC/UPC type pluggable optical interface
- ✧ Compliant with SFP MSA and SFF-8472 with simplex SC receptacle
- ✧ RoHS compliant and lead-free
- ✧ Single +3.3V power supply
- ✧ Support Digital Diagnostic Monitoring interface
- ✧ Case operating temperature Commercial: 0°C to +70°C



Applications

- ✧ Gigabit Ethernet
- ✧ Fiber Channel
- ✧ Switch to Switch interface
- ✧ Switched backplane applications
- ✧ Router/Server interface
- ✧ Other Optical Links

Ordering Information

Part Number	Output Power	Rec. Sens	Data Rate	Wavelength	Distance
FH-SB5412CDS80	-3 ~ 2 db	-28db	1.25G	TX1550/RX1490nm	80km

General

FH-SB5412CDS80 SFP-BIDI transceivers are high performance, cost effective modules supporting dual data-rate of 1.25Gbps/1.0625Gbps and 80km transmission distance with SMF. The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements. The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Note
Supply Voltage	Vcc	-0.5	4.0	V	
Storage Temperature	Ts	-40	85	°C	
Relative Humidity	RH	0	85	%	

Note: Stress in excess of the maximum absolute ratings can cause permanent damage to the module

General Operating Characteristics

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Data Rate	DR		1250		Gb/s	
Supply Voltage	Vcc	3.13	3.3	3.47	V	
Supply Current	Icc ₅			260	mA	
Operating Case Temp.	Tc	0		70	°C	
Operating Case Temp.	Tl	-40		85	°C	

Electrical Input/Output Characteristics

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Transmitter						
Diff. input voltage swing		120		820	mVpp	1
Tx Disable input	H	VIH	2.0	Vcc+0.3	V	
	L	VIL	0	0.8		
Tx Fault output	H	VOH	2.0	Vcc+0.3	V	2
	L	VOL	0	0.8		
Input Diff. Impedance	Zin		100		Ω	
Receiver						
Diff. output voltage swing		340	650	800	mVpp	3
Rx LOS Output	H	VOH	2.0	Vcc+0.3	V	2
	L	VOL	0	0.8		

Notes:

1. TD+/- are internally AC coupled with 100Ω differential termination inside the module.
2. Tx Fault and Rx LOS are open collector outputs, which should be pulled up with 4.7k to 10kΩ resistors on the host board. Pull up voltage between 2.0V and Vcc+0.3V.
3. RD+/- outputs are internally AC coupled, and should be terminated with 100Ω (differential) at the user SERDES.

Optical Characteristics

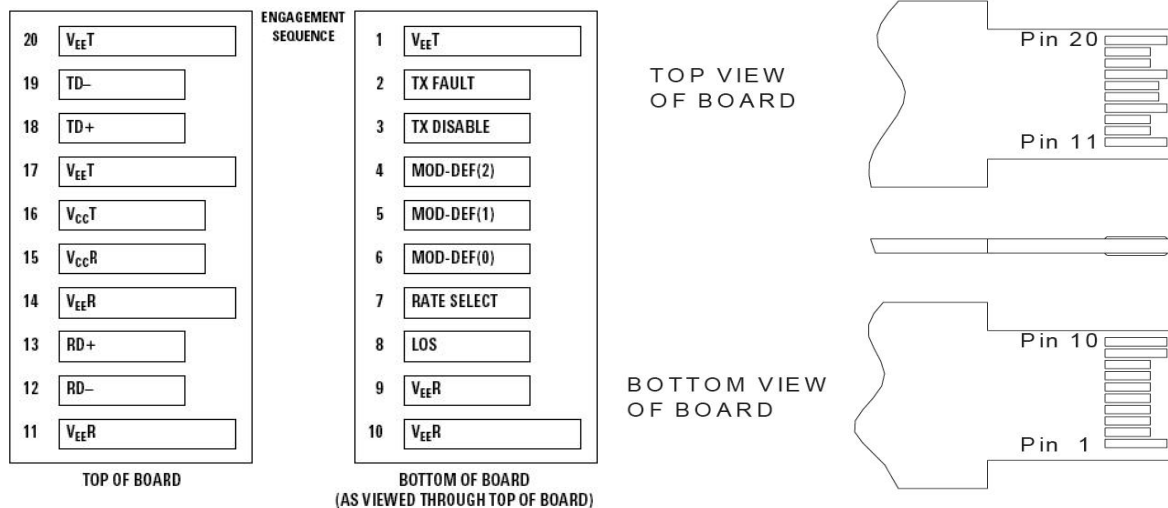
Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Transmitter						
Operating Wavelength	λ	1530	1550	1570	nm	
Ave. output power (Enabled)	PAVE	-3		2	dBm	1
Extinction Ratio	ER	9			dB	1
RMS spectral width	Δλ			4	nm	
Rise/Fall time (20%~80%)	Tr/Tf			0.26	ns	2
Dispersion penalty	TDP			3.9	dB	
Output Optical Eye	Compliant with IEEE802.3 z (class 1 aser safety)					

Receiver						
Operating Wavelength	λ	1470	1490	1510	nm	
Receiver Sensitivity	P _{SEN1}			-28	dBm	3
Overload	P _{AVE}	-3			dBm	
LOS Assert	P _a	-40			dBm	
LOS De-assert	P _d			-29	dBm	4
LOS Hysteresis	P _d -P _a	0.5		6	dB	

Notes:

1. Measured at 1250Mb/s with PRBS $2^{23}-1$ NRZ test pattern.
2. Unfiltered, measured with a PRBS $2^{23}-1$ test pattern @1.25Gbps
3. Measured at 1250Mb/s with PRBS $2^{23}-1$ NRZ test pattern for BER < 1×10^{-12}
4. When LOS de-asserted, the RX data+/- output is signal output.

Pin Definitions And Functions



Pin	Symbol	Name/Description	Notes
1	V _{EE} T	Tx ground	
2	Tx Fault	Tx fault indication, Open Collector Output, active “H”	1
3	Tx Disable	LVTTL Input, internal pull-up, Tx disabled on “H”	2
4	MOD-DEF2	2 wire serial interface data input/output (SDA)	3
5	MOD-DEF1	2 wire serial interface clock input (SCL)	3
6	MOD-DEF0	Model present indication	3
7	Rate select	No connection	

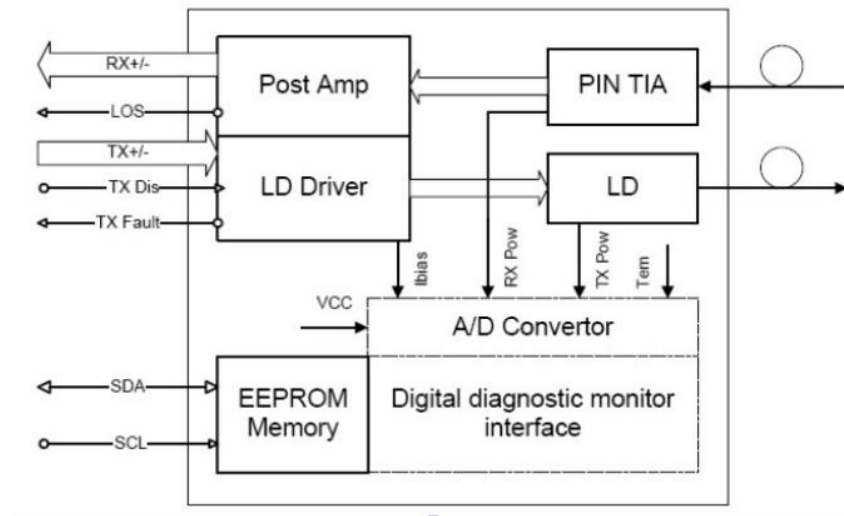
Diagnostics

Parameter	Symbol	Units	Min.	Max.	Accuracy	Note
Transceiver temperature	DTemp-E	°C	-45	+90	±5°C	1
Transceiver supply voltage	DVoltage	V	2.8	4.0	±3%	
Transmitter bias current	DBias	mA	2	80	±10%	2
Transmitter output power	DTx-Power	dBm	-3	+8	±3dB	
Receiver average input power	DRx-Power	dBm	-35	0	±3dB	

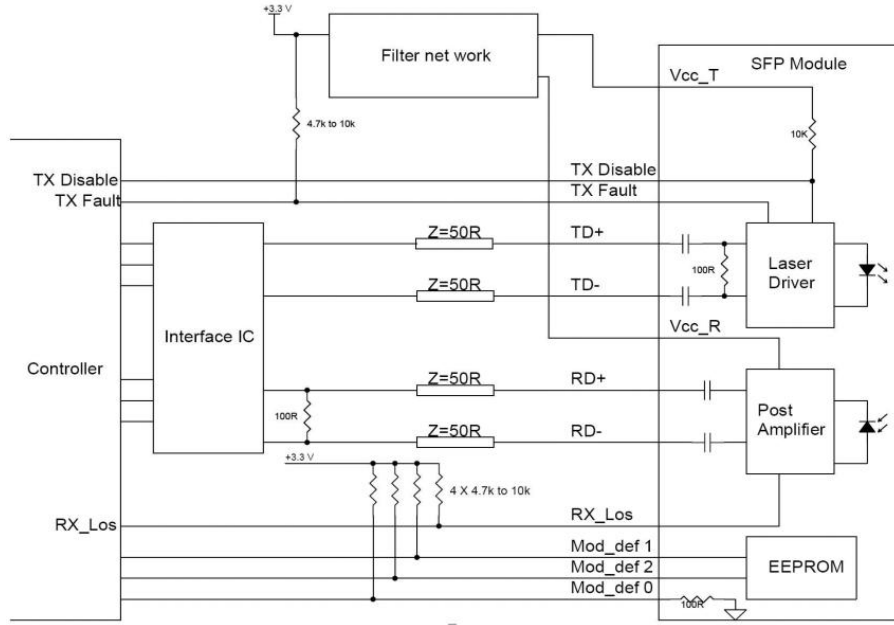
Notes:

1. When Operating temp.=0~70 °C, the range will be min=-5, Max=+75
2. The accuracy of the Tx bias current is 10% of the actual current from the laser driver to the laser
3. Internal/ External Calibration compatible.

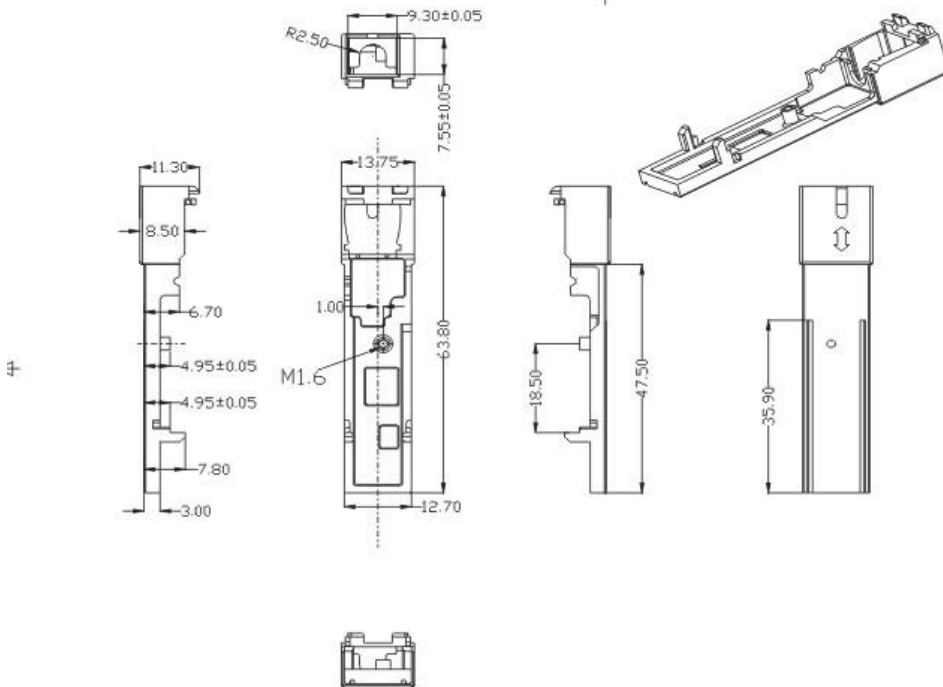
Functional Diagram

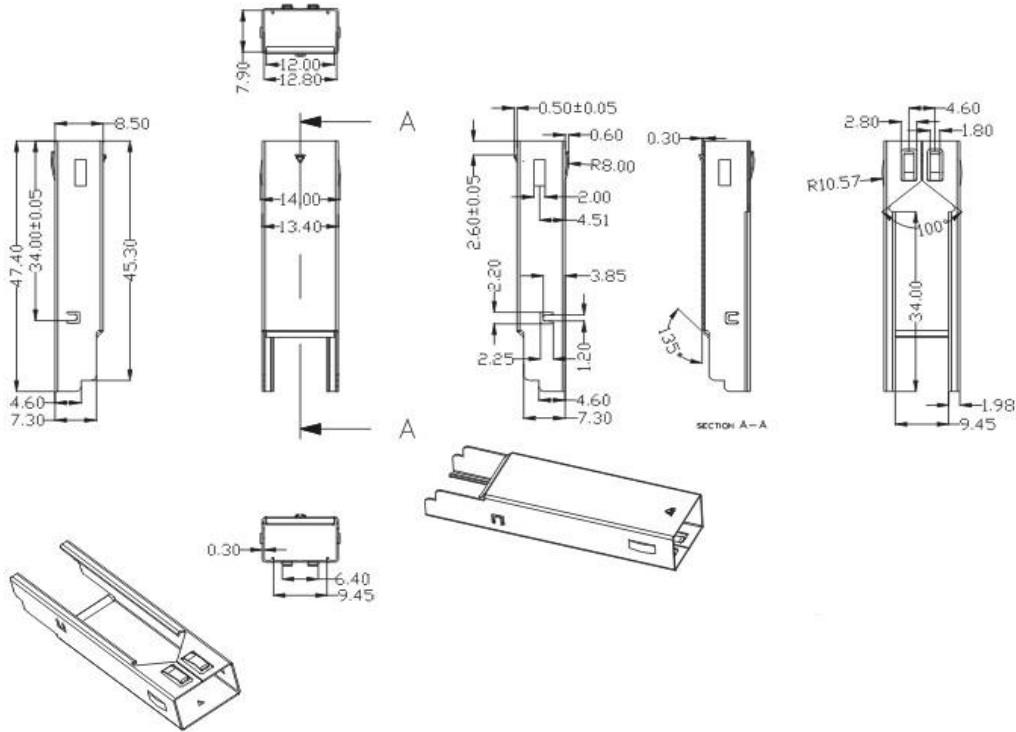


Typical Interface Circuit



Package Dimensions





For More Information

FANG HANG TECH LIMITED

Office : 908 Room, Jingyuan Building, 28 Bulong Rd, Longgang District, Shenzhen. China

Tel: +86-755-89584520

Fax: +86-755-89584520

sales@fanghangtech.com

www.fanghangtech.com